

Creating the board will be assisted by tech staff the specification has been outlined on the mounting of the boards or stability. It will be done in week 4. Activity 2 requires the board to be synchronized in which I started to look into linux and gui programs to help assist with the task to remove the Phase locked loop within the system to remove dithering which is contributing to the system not being synchronized. Activity 3 is getting the discrete time samples for the system to be observed. Another planned activity is getting the spatial time sampling however this task may not be completed due to limited time constraints on the project and final report.